

DXP-8000EC 8-Way Integrated Encoder



DXP-8000EC consists of two 4-way (total 8) H.264/MPEG-2 encoder modules, TS reMultiplexer, All encoders are real time and independent operated. Each encoder accepts one stereo program through its HDMI (HD or SD) input, or CVBS(SD) Input, These programs encoded are multiplexed onto one Transport Stream (TS). The essential PSI/SI tables of the TS, including LCN (Logical Channel Number), are generated automatically and are editable by user. This TS stream is also available through the TS/ IP port on the front panel with 3 modes: Full Duplex, 128 IPTV & 32 IPTV with 2 RJ-45 port.

DXP-8000EC is accommodated in a standard 1U x 19" rack mountable chassis.

Main Feature

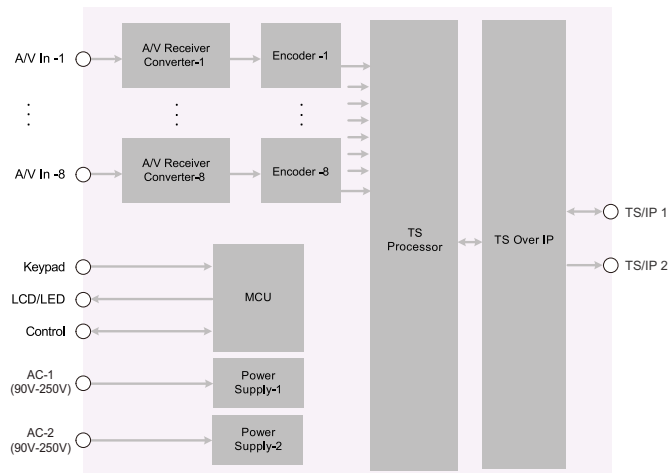
- Compliant with H.264/AVC HP@L4.0 & MPEG-2 MP@ML
- Support MPTS & SPTS, Re-multiplexed integrated
- Total bitrate up to 400Mb/s at 128 IPTV Mode
- Total bitrate up to 200Mb/s at 32 IPTV Mode with both RJ-45 port
- Total bitrate up to 80Mb/s at Full Duplex Mode
- HD/SD Video Resolution: 1080i, 720p, 576i, 480i
- MPEG1-L2, MPEG-2/4 AAC-LC/HE-AAC Audio Encode
- Remote control/monitoring with SNMP and HTTP/Web interface
- Two Redundant Power Supplies

Order Information

	Description	Encoder Input	Encoder Way	TS/IP (Full Duplex / IPTV Output)	Audio Encode (AAC)
1	DXP-8000EC-82H	HDMI	8	Y	Y
2	DXP-8000EC-42H		4	Y	Y
3	DXP-8000EC-80H		8	N	Y
4	DXP-8000EC-40H		4	N	Y
5	DXP-8000EC-82C	CVBS & Audio	8	Y	N
6	DXP-8000EC-42C		4	Y	N
7	DXP-8000EC-80C		8	N	N
8	DXP-8000EC-40C		4	N	N
9	DXP-8000EC-82S	SDI	8	Y	Y
10	DXP-8000EC-42S		4	Y	Y
11	DXP-8000EC-80S		8	N	Y
12	DXP-8000EC-40S		4	N	Y

Block Diagram

DXP-8000EC Functional Block Diagram



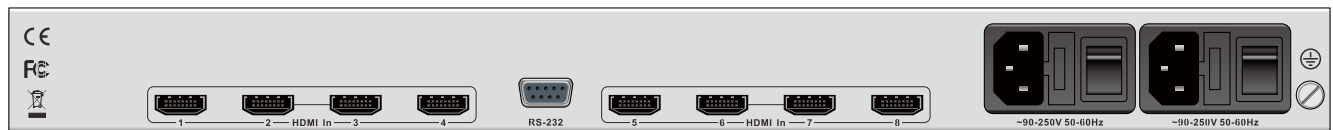
Specification

Video	
Input Interface	HDMI, type A (8000EC, HD/SD) RCA, 75Ω (8100EC, SD)
Encode Standard	H.264/AVC HP@L4.0&MPEG-2 MP@ML (8000EC, HD/SD) H.264/AVC MP@L4.0&MPEG-2 MP@ML (8100EC, SD)
Chrominance Sampling Format	4:2:0
Video Resolution & Recommend range for H.264	1080i(1920×1080)@25Hz,29.97Hz,30Hz :SMPTE274M: 1~13Mb/s 720p(1280×720)@25Hz,29.97Hz,30Hz:S MPTE296M: 1~13Mb/s 480i(720×480)@25Hz:SMPTE656M: 600K~10Mb/s 576i(720×576)@25Hz: SMPTE656M:600K~8Mb/s 480i(720×480)@25Hz:SMPTE656M: 3.5~8Mb/s 576i(720×576)@25Hz: SMPTE656M:3.5~8Mb/s
Video Resolution & Recommend range for Mpeg2	Manually enter, Arbitrarily
Other Output Resolution Aspect Ratio	4:3 or 16:9, configurable
Audio	
Input Interface	HDMI x 8 or RCA x 8 pair
Encode Standard	MPEG1 Layer II
Sampling Frequency	MPEG-2/4 AAC-LC, HE-AAC(V1,V2) 48KHz
Encoder Bit Rate Range	MPEG1 Layer II :32~192Kbps(Mono), 64~384Kbps(Stereo) MPEG2/4 AAC-LC :24~256Kbps(Mono), 48~512Kbps(Stereo) MPEG2/4 HE-AAC(V1/ V2): 16~128Kbps(Mono), 32~256Kbps(Stereo)

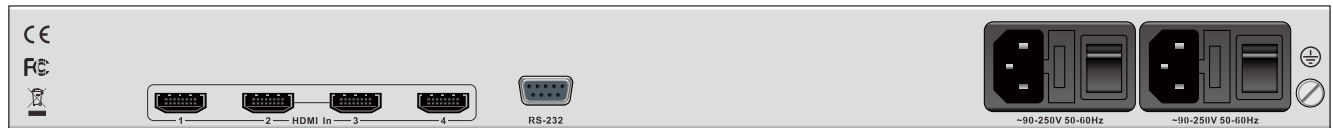
TS/IP, Mode 1: Full Duplex, Single TS in, Single TS out	
Standard	IEEE 802.3, 10/100/1000 Base-T
Maximum Bit Rate	80Mb/s in + 80Mb/s out
Data Encapsulation	UDP, RTP, SPTS or MPTS
Protocol	ICMP, ARP, IGMPv2/v3
TS/IP, Mode 2: 128 IPTV Output Only	
Standard	IEEE 802.3, 10/100/1000 Base-T
Maximum Output Bit Rate	400Mb/s
Data Encapsulation	UDP, RTP, SPTS or MPTS
Protocol	ICMP, ARP, IGMPv2/v3
TS/IP, Mode 3: 32 IPTV Output from two RJ-45 port	
Standard	IEEE 802.3, 10/100/1000 Base-T
Maximum Output Bit Rate	200Mb/s
Data Encapsulation	UDP, RTP, SPTS or MPTS
Protocol	ICMP, ARP, IGMPv2/v3
Rear Panel	
HDMI IN	HDMI (see model list) x 8
CVBS & Audio IN	RCA, 75Ω (see model list) x 8
Front Panel	
Control	RJ-45, 10/100 Base-T x 1, USB x 1
TS/IP	RJ-45X2, 10/100/1000 Base-T x 2; 1Back up Output only
Display	2x16 LCD module
Others	
Input AC Voltage Range	AC100~260V 50/60Hz
Operating Temperature	0~40°C
Storage Temperature	-10~60°C
Operating Humidity	10~90%, Non-condensed

Back Panel Interface

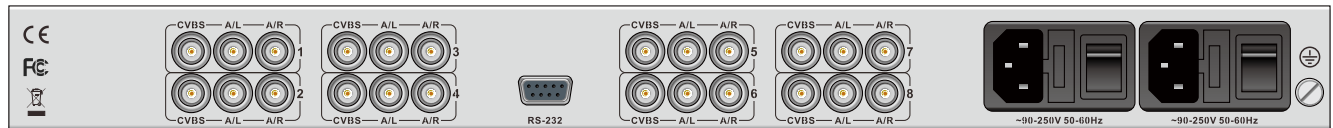
DXP-8000EC-82H



DXP-8000EC-42H



DXP-8000EC-82C



DXP-8000EC-42C

